TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC4078AP,TC74HC4078AF

8-Input OR/NOR Gate

The TC74HC4078A is a high speed CMOS 8-INPUT NOR GATE fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

Output X is an 8-INPUT NOR, output Y is an 8-INPUT OR.

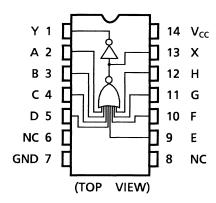
Each output has a buffer, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

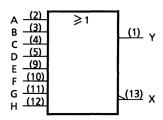
- High speed: $t_{pd} = 13 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 1 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$
- High noise immunity: VNIH = VNIL = 28% VCC (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: t_pLH ≃ t_pHL
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 4078B

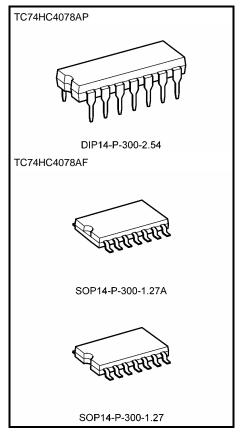
Pin Assignment



NC: No connection

IEC Logic Symbol





Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.)

2006-02-01



Truth Table

Inputs A Through H	Outputs				
Inputs A Through Th	X	Υ			
All Inputs L	Н	L			
All Other Combination	L	Н			

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	–0.5 to 7	V
DC input voltage	VIN	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	l _{OUT}	±25	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	P _D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The recommended operating conditions are required to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol		Test Condition		-	Га = 25°(Ta = -40 to 85°C		Unit
Ondraolonolos Oynik	Cymbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		_		2.0	1.50	_	_	1.50	_	
High-level input voltage	V _{IH}			4.5	3.15	_	_	3.15	_	V
ŭ				6.0	4.20	_		4.20	_	
				2.0	_	_	0.50	_	0.50	
Low-level input voltage	VIL		_		_	_	1.35	_	1.35	V
ű				6.0	_	_	1.80	_	1.80	
	Voн	V _{IN} = V _{IH} or V _{IL}		2.0	1.9	2.0		1.9	_	
			$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80		5.63	_	
		V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage	V _{OL}			6.0	_	0.0	0.1	_	0.1	V
ŭ			I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	1
			I _{OL} = 5.2 mA	6.0	_	0.18	0.26	_	0.33	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0		_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{C}$	V _{IN} = V _{CC} or GND		_	_	1.0	_	10.0	μА

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}	_	_	4	8	ns
Propagation delay time	t _{pLH} t _{pHL}	_	_	13	22	ns



AC Characteristics (C_L = 50 pF, input: $t_r = t_f = 6$ ns)

Characteristics Sy	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
	4		2.0	_	30	75	_	95	
Output transition time		_	4.5	_	8	15	_	19	ns
	t _{THL}		6.0	_	7	13	_	16	
Propagation delay tpLH time	4		2.0	_	50	130	_	165	
	·	_	4.5	_	16	26	_	33	ns
	t _{pHL}		6.0	_	14	22	_	28	
Input capacitance	C _{IN}	_	•	_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)		40		_		pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

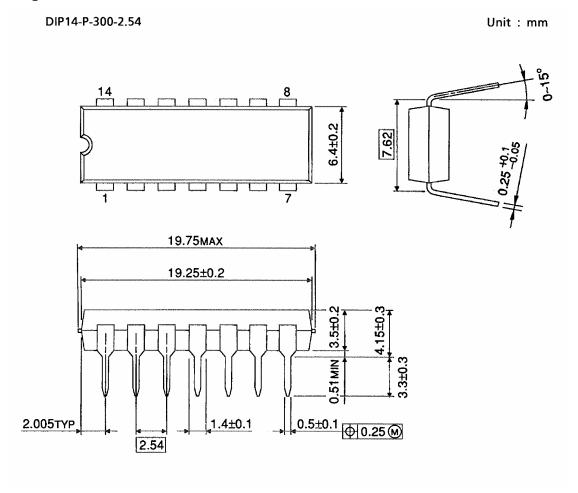
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Average operating current can be obtained by the equation:

$$ICC (opr) = Cpd \cdot Vcc \cdot fIN + ICC$$

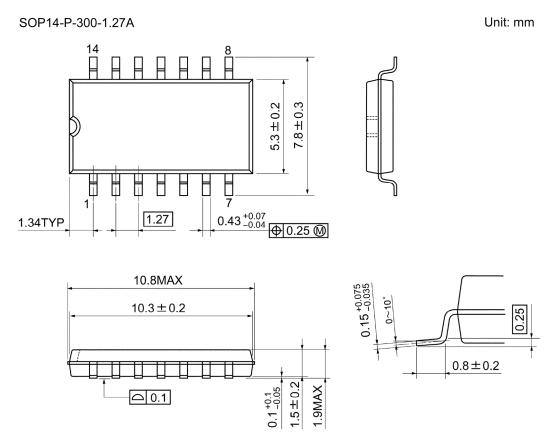


Package Dimensions



Weight: 0.96 g (typ.)

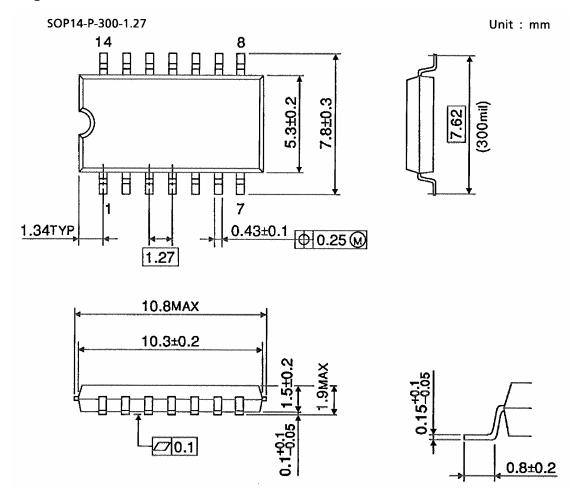
Package Dimensions



Weight: 0.18 g (typ.)



Package Dimensions



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Weight: 0.18 g (typ.)

Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A

RESTRICTIONS ON PRODUCT USE

Handbook" etc. 021023_A

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